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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO.	
10/634,876	08/06/2003	Hideki Iwata	1713.1008	8217
21171 STAAS & HAI	7590 03/18/200 SEY LLP	EXAMINER		
SUITE 700		ROJAS, BERNARD		
WASHINGTO	RK AVENUE, N.W. N, DC 20005		ART UNIT	PAPER NUMBER
			2832	
			MAIL DATE	DELIVERY MODE
			03/18/2009	PAPER

## Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Supplemental					
Notice	of Allowability				

Application No.	Applicant(s)	
10/634,876	IWATA ET AL.	
Examiner	Art Unit	
BERNARD ROJAS	2832	

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	BERNARD ROJAS	2832					
The MAILING DATE of this communication apperation All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313 1.  ☐ This communication is responsive to the certified copy of the communication.	(OR REMAINS) CLOSED in this app or other appropriate communication <b>GHTS.</b> This application is subject to and MPEP 1308.	olication. If not include will be mailed in due	ed course. <b>THIS</b>				
2. X The allowed claim(s) is/are 1.5-8.11.15.18.22.24.27.30-38.	40,43,44,46 and 68.						
2.							
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 7. Examiner's Amendn 8. Examiner's Stateme 9. Other	(PTO-413), e nent/Comment	wance				

## **DETAILED ACTION**

## **Priority**

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

## Allowable Subject Matter

Claims 1, 5-8, 11, 15, 18, 22, 24, 27, 30-38, 40, 43, 44, 46 and 68 are allowed.

The following is an examiner's statement of reasons for allowance:

Claims 1, 15, 18, 24, 27, 30-38 and 68, the prior art of record does not teach nor suggest, in the claimed combination, a micro-relay with a first substrate having stationary contacts and a stationary electrode; a second substrate, having a stationary contact, arranged so as to face the first substrate; and a frame sandwiched between the first and second substrates to realize a hermetical sealed structure, a movable portion with multiple movable contacts parallel relative to the first and second substrates and attached to the frame via springs wherein the multiple movable contacts are located between the stationary contacts without making contact with any of the stationary contacts of the first and second substrates when a voltage is not applied between the movable electrode and the stationary electrode, and when the multiple movable contacts are released from a connecting state in which the movable contacts make a connection with the stationary contacts of the first substrates, the movable contacts are disconnected therefrom and make a connection with the stationary contact of the second substrate that is at a ground potential.

Claim 43, the prior art of record does not teach nor suggest, in the claimed combination, a micro-relay further comprising a base substrate that supports the first substrate, members that connect the movable electrode and the stationary electrode to pads formed on the base substrate, and resin that covers the first and second substrates and the movable plate.

Claim 44, the prior art of record does not teach nor suggest, in the claimed combination, a micro-relay comprising: a first substrate having stationary contacts and a stationary electrode; a second substrate arranged so as to face the first substrate; and a movable plate arranged between the first and second substrates, the movable plate having a frame and a movable portion, the frame being sandwiched between the first and second substrates to realize a hermetical sealed structure, the movable portion having a movable electrode facing the stationary electrode, and a movable contact facing the stationary contacts, the movable portion moving between the first and second substrates due to electrostatic attraction that develops between the movable electrode and the stationary electrode, wherein the frame has a protrusion and the movable portion has a counterpart recess.

Claim 46, the prior art of record does not teach nor suggest, in the claimed combination, a micro-relay comprising: a first substrate having stationary contacts and a stationary electrode; a second substrate arranged so as to face the first substrate; and a movable plate arranged between the first and second substrates, the movable plate having a frame and a movable portion, the frame being sandwiched between the first and second substrates to realize a hermetical sealed structure, the movable portion

having a movable electrode facing the stationary electrode, and a movable contact facing the stationary contacts, the movable portion moving between the first and second substrates due to electrostatic attraction that develops between the movable electrode

and the stationary electrode, wherein the movable portion and the frame have an

identical thickness.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Iwata et al. [US 6,828,888] discloses multiple features of the claimed invention but does not constitute prior art due to it's filing date.
- Miller et al. [US 5,372,515] discloses a mechanical ESD protector.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to BERNARD ROJAS whose telephone number is (571)272-1998. The examiner can normally be reached on M and W-F, 10:00-7:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Elvin G. Enad can be reached on (571) 272-1990. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information

/Elvin G Enad/ Supervisory Patent Examiner, Art Unit 2832

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Br /Bernard Rojas/ Examiner, Art Unit 2832